

Ministry of Higher Education and Scientific Research
University of Oum El Bouaghi
Academic Year 2023/2024

Institute of technology

Department: Network and telecommunications

First Semester , 1st Year Master (M1)

| | 08h00 - 09h30 | 09h30 - 11h00 | 11h00 - 12h30 | | 14h00 - 15h30 | 15h30 - 17h00 |
|------------------|----------------------------------------|----------------------------------------------|------------------------------------------------------------------|--|---------------------------------------------------------------------------------------|---------------|
| SATURDAY | | | Course TeEn Online | | | |
| SUNDAY | Course FSP BENGHALIA Class 16 | (Course +Ts)/15 ADC HAMOUDA Class 31 | Ts/15 FSP BENGHALIA Class 31 Ts/15 ADC HAMOUDA Class 31 | | | |
| MONDAY | Course FPGA&HDL BENACER Class 32 | PW ADC (G1/G2)/15 Labo Telecom HAMOUDA | | | PW: FPGA&HDL (Labo Info 3) G2/G1 BENACER | |
| | | PW S&S (G2/G1) /15 Labo Info 3 ROSTOM | | | PW RIP (G1/G2)/15 Labo info 1 MELLAL | |
| TUESDAY | | PW: FSP (G2/G1)/15 labo telecom MAHAMDI | | | PW S&RP (G1/G2)/15 Workshop MOULAHCENE | |
| | | PW DWD (G1/G2)/15 Labo Info 2 MERAZGA | | | | |
| WEDNESDAY | | Course/Ts/15 C&C BOUCHEOUARB Class S12 | Course S&S MAARAD Class S12 | | PW C&C (G1/G2)/15 labo info 2 BOUCHEOUARB (Ts)/15 (S&S)G2/G1 MAARAD Class 17 | |
| THURSDAY | Course /15 RIP MELLAL Class 27 | Ts /15 RIP MELLAL Class 27 | | | | |

| <i>Abbreviation</i> | <i>Modules</i> | <i>Teacher-Responsible</i> |
|---------------------|---------------------------------------------------------|-------------------------------|
| <i>FSP</i> | <i>Free Space Propagation</i> | <i>BENGHALIA ABEDELMADJID</i> |
| <i>C&C</i> | <i>Coding and Compression</i> | <i>BOUCHOUAREB RACHIDA</i> |
| <i>ADC</i> | <i>Advanced Digital Communications</i> | <i>HAMOUDA AMMAR</i> |
| <i>RIP</i> | <i>Routing IP</i> | <i>MELLAL NASSIMA</i> |
| <i>DWD</i> | <i>Database and Web Development</i> | <i>MERAZGA AMMAR</i> |
| <i>S&S</i> | <i>Signals and Systems</i> | <i>MAARAD ILHAM</i> |
| <i>FPGA&HDL</i> | <i>FPGA et Langages de description matérielle (HDL)</i> | <i>BENACER IMAD</i> |
| <i>S&RP</i> | <i>Study and Realization of Projects</i> | <i>MOULAHCENE FATEH</i> |
| <i>TeEn</i> | <i>Technical English</i> | |
| <i>PW</i> | <i>Practical Work</i> | |
| <i>Ts</i> | <i>Tutorials</i> | |
| <i>Class</i> | <i>Classroom</i> | |