Curriculum Vitae

Personal information

- ***Personal***

– Prof. **Imad Benacer**

– Institute of Science and Applied Technology (SAT Institute), Department of Telecommunication and Networking, University of Oum El Bouaghi, Algeria.

– E-mail: **benacer.imad@univ-oeb.dz / benacerimad@gmail.com**

– Mobile: **0666 42 13 43**

- ***Researcher identity***
  - Google Scholar: https://scholar.google.com/citations?hl=fr&user=SApXrR0AAAAJ
  - ORCID: https://orcid.org/0000-0002-3063-9695

Education

– **Ph.D.'s degree** in Micro-electronic

– **Magister degree** in Micro-electronic

– **Engineering**'s degree in Electronic

Functions and Affiliations

– **Associate professor,** Department of Telecommunication and Networking, University of Oum El Bouaghi, Algeria

Teaching modules

License & Master

- Informatics,
- Personal and Professional Project (FPGA),
- Electronic,
- Personal and Professional Project (Arduino),
- FPGAs and Hardware Description Languages (HDL).

International publications

– *I. Benacer and Z. DIBI, "Modeling and Simulation of Organic Field EffectTransistor (OFET) Using Artificial Neural Networks," International Journalof Advanced Science & Technology, vol. 66, 2014.*

- F. Moulahcene, N. Bouguechal, I. Benacer, "Design of CMOS Two stage Operational Amplifier for ECG Monitoring System Using 90nm Technology," International Journal of Automation and Computing, Vol. 6, Issue. 5, pp. 55-66, 2014.
- I. Benacer and Z. DIBI, "Extracting Parameters of OFET before and after Threshold Voltage using Genetic Algorithms," International Journal of Automation and Computing, Vol. 13, Issue. 4, 2016.
- F. Bouguerra, I. Benacer, L. Saidi" MLP and RBF symbol tracking with 16 QAM modulation over multipath distorted channel," International Conference on Advanced Systems and Electric Technologies, 2017.
- F. Moulahcene, I. Benacer, "Chopper-stabilized Fully Differential Amplifier for Portable ECG Monitoring Systems using 90 nm Technology," International Conference on on Electrical, Computer and Energy Technologies (ICECET), 2021.

Interests and Qualifications

My research interest includes modeling and simulation of TFT using Organicsemiconductor, Artificial intelligence. In addition, I am interested in programing FPGA, digital and Mixedsignal for integrated circuits and Real-time systems.